

Refine Search

Search Results -

Term	Documents
STAL\$4	0
STAL	441
STALA	1
STALABLE	1
STALACR]	1
STALACTI	1
STALAGMA	1
STALAK	1
STALAM	2
STALAN	1
STALAND	2
(L3 AND (STAL\$4 OR INHIBIT\$3)).USPT.	3

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DB=USPT; PLUR=YES; OP=OR

<u>L4</u>	L3 and (stal\$4 or inhibit\$3)	3	<u>L4</u>
<u>L3</u>	L1 and redundan\$4	18	<u>L3</u>
<u>L2</u>	L1 and redundant near5 thread\$3	3	<u>L2</u>
<u>L1</u>	thread\$2 near6 count\$3 near5 (stor\$6 or sav\$3 or duplicat44 or replicat\$4)	78	<u>L1</u>

END OF SEARCH HISTORY

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Search Results -

Term	Documents
"5802265"	1
5802265S	0
THREAD\$2	0
THREAD	1749
THREADED	4451
THREADER	8
THREADID	2
THREADLY	5
THREADS	1850
THREADUP	2
THREADY	1
(5802265.PN. AND THREAD\$2).USPT.	1

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Name
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<u>L16</u>	5802265.pn. and thread\$2	1	<u>L16</u>
<u>L15</u>	context near7 (cycle\$1 or clock\$1) near6 count\$3 and thread\$2	4	<u>L15</u>
<u>L14</u>	context near7 (cycle\$1 or clock\$1) near6 count\$3 near15 thread\$2	0	<u>L14</u>
<u>L13</u>	context near7 (cycle\$1 or clock\$1) near6 count\$3 near9 thread\$2	0	<u>L13</u>
<u>L12</u>	context near7 cycle\$1 near6 count\$3 near9 thread\$2	0	<u>L12</u>
<u>L11</u>	context near5 cycle\$1 near3 count\$3 near7 thread	0	<u>L11</u>
<u>L10</u>	context near5 count\$3 near7 thread	18	<u>L10</u>
<u>L9</u>	context near5 count\$3	738	<u>L9</u>
<u>L8</u>	L6 and redundan\$3	9	<u>L8</u>
<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>			
<u>L7</u>	L6 and redundan\$3	9	<u>L7</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L6</u>	thread\$2 near8 (cycle\$1 or clock\$1) near5 count\$3	72	<u>L6</u>
<u>L5</u>	thread\$2 and (cycle\$1 or clock\$1) near5 count\$3	4091	<u>L5</u>
<u>L4</u>	L3 and (stal\$4 or inhibit\$3)	3	<u>L4</u>
<u>L3</u>	L1 and redundan\$4	18	<u>L3</u>
<u>L2</u>	L1 and redundant near5 thread\$3	3	<u>L2</u>
<u>L1</u>	thread\$2 near6 count\$3 near5 (stor\$6 or sav\$3 or duplicat44 or replicat\$4)	78	<u>L1</u>

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Results Key:

JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

1 Transient fault detection via simultaneous multithreading

Reinhardt, S.K.; Mukherjee, S.S.;

Computer Architecture, 2000. Proceedings of the 27th International Symposium , 10-14 June 2000

Pages:25 - 36

[\[Abstract\]](#) [\[PDF Full-Text \(1412 KB\)\]](#) IEEE CNF

2 A nested invocation suppression mechanism for active replication fault-tolerant CORBA

Deron Liang; Chen-Liang Fang; Chyohwa Chen; Fengyi Lin;

Software Engineering Conference, 2002. Ninth Asia-Pacific , 4-6 Dec. 2002

Pages:117 - 125

[\[Abstract\]](#) [\[PDF Full-Text \(409 KB\)\]](#) IEEE CNF

3 Performance evaluation of a fault-tolerant mechanism based on replicated distributed objects for CORBA

Takemoto, M.; Nakamura, T.;

Object-Oriented Real-Time Distributed Computing, 2001. ISORC - 2001. Proceedings. Fourth IEEE International Symposium on , 2-4 May 2001

Pages:95 - 102

[\[Abstract\]](#) [\[PDF Full-Text \(596 KB\)\]](#) IEEE CNF

4 Deterministic scheduling for transactional multithreaded replicas

Jimenez-Peris, R.; Patino-Martinez, M.; Arevalo, S.;

Reliable Distributed Systems, 2000. SRDS-2000. Proceedings The 19th IEEE Symposium on , 16-18 Oct. 2000

Pages:164 - 173

[\[Abstract\]](#) [\[PDF Full-Text \(904 KB\)\]](#) [IEEE CNF](#)

5 Enforcing determinism for the consistent replication of multithreaded CORBA applications

Narasimhai, P.; Moser, L.E.; Melliar-Smith, P.M.;

Reliable Distributed Systems, 1999. Proceedings of the 18th IEEE Symposium , 19-22 Oct. 1999

Pages:263 - 273

[\[Abstract\]](#) [\[PDF Full-Text \(384 KB\)\]](#) [IEEE CNF](#)

6 Advances in computational resiliency

Lee, J.; Taylor, S.;

Aerospace Conference, 2001, IEEE Proceedings. , Volume: 6 , 10-17 March 2001

Pages:2829 - 2835 vol.6

[\[Abstract\]](#) [\[PDF Full-Text \(664 KB\)\]](#) [IEEE CNF](#)

7 A nested invocation suppression framework for active replication fault-tolerant CORBA

Deron Liang; Chen-Liang Fang; Chyauhwa Chen; JiChiang Tsai;

Computer Software and Applications Conference, 2002. COMPSAC 2002.

Proceedings. 26th Annual International , 26-29 Aug. 2002

Pages:757 - 762

[\[Abstract\]](#) [\[PDF Full-Text \(417 KB\)\]](#) [IEEE CNF](#)

8 Assuring consistency and increasing reliability in group communication mechanisms in computational resiliency

Lucena, N.; Chapin, S.J.; Lee, J.;

Information Assurance Workshop, 2003. IEEE Systems, Man and Cybernetics Society , 18-20 June 2003

Pages:135 - 142

[\[Abstract\]](#) [\[PDF Full-Text \(970 KB\)\]](#) [IEEE CNF](#)

9 Transient-fault recovery using simultaneous multithreading

Vijaykumar, T.N.; Pomeranz, I.; Cheng, K.;

Computer Architecture, 2002. Proceedings. 29th Annual International Symposium on , 25-29 May 2002

Pages:87 - 98

[\[Abstract\]](#) [\[PDF Full-Text \(288 KB\)\]](#) [IEEE CNF](#)

10 A study of slipstream processors

Purser, Z.; Sundaramoorthy, K.; Rotenberg, E.;

Microarchitecture, 2000. MICRO-33. Proceedings. 33rd Annual IEEE/ACM International Symposium on , 10-13 Dec. 2000

Pages:269 - 280

[\[Abstract\]](#) [\[PDF Full-Text \(1024 KB\)\]](#) [IEEE CNF](#)

11 Semi-passive replication

Defago, X.; Schiper, A.; Sergent, N.;

Reliable Distributed Systems, 1998. Proceedings. Seventeenth IEEE Symposium , 20-23 Oct. 1998

Pages:43 - 50

[\[Abstract\]](#) [\[PDF Full-Text \(128 KB\)\]](#) **IEEE CNF**

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<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>			
<u>L35</u>	L34 not l2	10	<u>L35</u>
<u>L34</u>	l1 and L33	19	<u>L34</u>
<u>L33</u>	L32 or l24	27715	<u>L33</u>
<u>L32</u>	(714/1-57)[CCLS]	11911	<u>L32</u>
<u>L31</u>	(714/1-824)![CCLS]	27279	<u>L31</u>
<u>L30</u>	(714/1-824)[CCLS]	27279	<u>L30</u>
<u>L29</u>	radundan\$3 near5 thread\$2	0	<u>L29</u>
<u>L28</u>	L27 and radundan\$3	0	<u>L28</u>
<u>L27</u>	(replicat\$4 or duplicat\$4 or cop\$3) near4 count\$3 near7 thread\$2	26	<u>L27</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L26</u>	L24 and l15	9	<u>L26</u>
<u>L25</u>	L24 and l18	2	<u>L25</u>

<u>L24</u>	l19 or l21 or l22 or L23	16488	<u>L24</u>
<u>L23</u>	(718/100-108)![CCLS]	3463	<u>L23</u>
<u>L22</u>	(719/311-332)![CCLS]	3519	<u>L22</u>
<u>L21</u>	(717/100/178)![CCLS]	631	<u>L21</u>
<u>L20</u>	(712/2-300)[CCLS]	9556	<u>L20</u>
<u>L19</u>	(712/2-300)![CCLS]	9556	<u>L19</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L18</u>	L17 not l2	21	<u>L18</u>
<u>L17</u>	L14 and trail\$3 near5 thread\$2 and (cop\$3 or replicat\$4 or duplication\$1 or sav\$3 or keep\$4 or kept or stor\$5 or held or hold\$3) near5 (count\$3 or clock\$1)	30	<u>L17</u>
<u>L16</u>	L14 and trail\$3 near5 thread\$2 (cop\$3 or replicat\$4 or duplication\$1 or sav\$3 or keep\$4 or kept or stor\$5 or held or hold\$3) near5 (count\$3 or clock\$1)	176872	<u>L16</u>
<u>L15</u>	L14 and trail\$3 near5 thread\$2	1035	<u>L15</u>
<u>L14</u>	lead\$3 near4 thread\$2	25572	<u>L14</u>
<u>L13</u>	L12 not l2	5	<u>L13</u>
<u>L12</u>	(thread\$3 near4 redundan\$3) near9 fault\$2	14	<u>L12</u>
<u>L11</u>	L10 not l2	13	<u>L11</u>
<u>L10</u>	L9 near15 fault\$2	22	<u>L10</u>
<u>L9</u>	(thread\$3 near4 redundan\$3 or srt)	2033	<u>L9</u>
<u>L8</u>	L7 not l2	9	<u>L8</u>
<u>L7</u>	l1 and redundan\$3	18	<u>L7</u>
<u>L6</u>	l1 not l2	51	<u>L6</u>
<u>L5</u>	l1 not l2 L2	63	<u>L5</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L4</u>	5933860.pn.	1	<u>L4</u>
<u>L3</u>	5758142.pn.	1	<u>L3</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L2</u>	L1 and (sav\$3 or stor\$3 or cop\$4 or duplicat\$4 or replicat\$4) near5 (count\$3 or clock\$1)	12	<u>L2</u>
<u>L1</u>	(plural\$7 or multiple or two or second\$4 or redundan\$4 or backup or identical) near5 thread\$2 near6 fault\$2	63	<u>L1</u>

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Term	Documents
(15 AND 24).PGPB,USPT.	9
(L24 AND L15).PGPB,USPT.	9

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<u>L25</u>	L24 and l18	2	<u>L25</u>
<u>L24</u>	l19 or l21 or l22 or L23	16488	<u>L24</u>
<u>L23</u>	(718/100-108)![CCLS]	3463	<u>L23</u>
<u>L22</u>	(719/311-332)![CCLS]	3519	<u>L22</u>
<u>L21</u>	(717/100/178)![CCLS]	631	<u>L21</u>
<u>L20</u>	(712/2-300)[CCLS]	9556	<u>L20</u>
<u>L19</u>	(712/2-300)![CCLS]	9556	<u>L19</u>
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L18</u>	L17 not l2	21	<u>L18</u>
<u>L17</u>	L14 and trail\$3 near5 thread\$2 and (cop\$3 or replicat\$4 or duplication\$1 or sav\$3 or keep\$4 or kept or stor\$5 or held or hold\$3) near5 (count\$3 or	30	<u>L17</u>

	clock\$1)		
<u>L16</u>	L14 and trail\$3 near5 thread\$2 (cop\$3 or replicat\$4 or duplication\$1 or sav\$3 or keep\$4 or kept or stor\$5 or held or hold\$3) near5 (count\$3 or clock\$1)	176872	<u>L16</u>
<u>L15</u>	L14 and trail\$3 near5 thread\$2	1035	<u>L15</u>
<u>L14</u>	lead\$3 near4 thread\$2	25572	<u>L14</u>
<u>L13</u>	L12 not l2	5	<u>L13</u>
<u>L12</u>	(thread\$3 near4 redundan\$3) near9 fault\$2	14	<u>L12</u>
<u>L11</u>	L10 not l2	13	<u>L11</u>
<u>L10</u>	L9 near15 fault\$2	22	<u>L10</u>
<u>L9</u>	(thread\$3 near4 redundan\$3 or srt)	2033	<u>L9</u>
<u>L8</u>	L7 not l2	9	<u>L8</u>
<u>L7</u>	l1 and redundan\$3	18	<u>L7</u>
<u>L6</u>	l1 not l2	51	<u>L6</u>
<u>L5</u>	l1 not l2 L2	63	<u>L5</u>
	<i>DB=USPT; PLUR=YES; OP=OR</i>		
<u>L4</u>	5933860.pn.	1	<u>L4</u>
<u>L3</u>	5758142.pn.	1	<u>L3</u>
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L2</u>	L1 and (sav\$3 or stor\$3 or cop\$4 or duplicat\$4 or replicat\$4) near5 (count\$3 or clock\$1)	12	<u>L2</u>
<u>L1</u>	(plural\$7 or multiple or two or second\$4 or redundan\$4 or backup or indential) near5 thread\$2 near6 fault\$2	63	<u>L1</u>

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